PROBLEMS

9-1 STOP condition
9-5b, the PIC signals the line low in acknowledgment of the SDA and SCL lines (a) Draw the signals on the STOP condition
(b) Now redraw these SDA low after the 1 in transmitting one

9-2 I2C bus timing as described at the begin
(a) Create a chart like kilb/s transfers.
(b) Some of the "cycles" s. For example, of Figure 9.7, create COUNT variable w been decremented


goto
will branch back to

9-3 Direct addressing
and clearing bits of the less instructions to exec assuming that the indirect beginning of the exec
Another reason to discussion of Section 4 of P4.ASM (Figure 5-7 use direct addressing to

9-4 DAC output Write to make the OUT1 ana

9-5 I2C subroutine
(a) Using the I2Cin s data into DATAIN
(b) Assuming the LM power-on default sature into DATAIN.